

	AXOL1TL	CICADA
<i>Model Parameters</i>		
Latency	50 ns	81.25 ns
Clock cycles	2	13
Clock frequency	40 MHz	160 MHz
Number of parameters	2 187	4 112
<i>Resource Usage on Virtex-7 FPGA</i>		
LUT	18 961 (4.3%)	85 005 (19.6%)
FF	424 (0.05%)	119 509 (14%)
BRAM	0 (0%)	177.50 (12%)
DSP	0 (0%)	764 (21%)